

U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

Art Unit: 2813

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

Claims 1-3, 8-10, 14 are allowed because none of the prior art alone or in combination teaches or suggests the particular subset of the semiconductor device having the drift region, impurities region, source/drain region have the same conductivity type, wherein the impurities layer is more highly doped than the drift region, which extend deeper into the semiconductor layer than the impurities layer, and the drain region is in direct contact with the drift region.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thanh Nguyen whose telephone number is (571) 272-1695, or by Email via address Thanh.Nguyen@uspto.gov. The examiner can normally be reached on Monday-Thursday from 6:00AM to 3:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr., can be reached on (571) 272-1702. The fax phone number for this Group is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent
Application Information Retrieval (PAIR) system. Status information for published applications

Art Unit: 2813

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pairdirect.uspto.gov. Should you have questions on access to thy Private PAIR system, contact the Electronic Business center (EBC) at 866-217-9197 (toll-free).

Thanh Nguyen

Patent Examiner

Patent Examining Group 2800

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